



अखिल भारतीय तकनीकी शिक्षा परिषद्
All India Council for Technical Education

ISO:9001:2015 CERTIFIED

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SIRINTEL TECHNOLOGIES

IT TRAINING & DEVELOPMENT

VISIT WWW.SIRINTEL.COM FOR DETAILS
AICTE APPROVED ORGANIZATION
TRAINER: SHIVALI S Ph: +91-8971279127



SCAN QR CODE



ABOUT US

SIRINTEL TECHNOLOGIES IS A TOPMOST SERVICE PROVIDER BASED ON INFORMATION TECHNOLOGY (IT), WHO ENSURES TO PROVIDE ESSENTIAL SOLUTION FOR SOFTWARE NEEDS WITH PREMIUM RESULTS. WE HAVE A TEAM OF PROFESSIONALS WHO ARE HIGHLY UNSWERVING AND FOCUSED ON FIRST-CLASS CUSTOMER SERVICE TO EARN THEIR FULLEST CONTENTMENT. OUR MAIN AIM IS TO BRING SUREFIRE RESULTS THROUGH OUR HARD WORK IN ORDER TO DEVELOP ENDURING RELATIONSHIPS WITH OUR CUSTOMERS. WE VANITY OURSELVES FOR HAVING PIONEERING TALENT AND AGILITY FOR OFFERING RIGHT SOLUTION FOR OUR VALUABLE CUSTOMERS AND IN PROVIDING THEM WITH A WONDERFUL SUPPORT NETWORK WHICH IS MORE ANALOGOUS TO DEVELOP PERSONAL RELATIONSHIP APART FROM JUST CREATING BUSINESS PARTNERSHIP.

WE ARE RENOWNED TO OFFER EXPERT DELIVERY OF SERVICES!

OUR CAMPAIGN HAS OWNED SOLID REPUTATION THROUGH PROFESSIONALISM AND NOT THROUGH NONSENSE APPROACH TO MEET THE CHALLENGES PRESENTED BY OUR CUSTOMER'S SUPPORT REQUIREMENTS AND ONGOING NEEDS. WE ARE RENOWNED TO OFFER EXPERT DELIVERY OF SERVICES THROUGH OUR HIGHLY-TRAINED AND QUALIFIED TECHNICIANS. LATEST ADVANCEMENTS TO IT SERVICES HAD LAGGED HUMAN RESOURCES, NEW TECHNOLOGIES AND SKILL LEVEL CHALLENGES ARE THE SIGNIFICANT REASONS TO GET OUR HELP TO TRANSFORM BUSINESS OPERATIONS WITH VITAL METHODS WHICH YOU WANT. OUR CUSTOMERS BELONGING TO VARIOUS NICHES HAVE GAINED SIGNIFICANT RESULTS IN BUSINESS BY EMPLOYING OUR TOP-NOTCH SOLUTION AND HAVE APPRECIATED US FOR HARD WORK AND EFFORTS. WE ALWAYS RANK NUMBER ONE IN SATISFACTION OF CUSTOMERS.

WHY IN-PLANT TRAINING IMPORTANT

IPT, ALSO KNOWN AS INPLANT TRAINING, IS A WIDELY SOUGHT AFTER PRACTICE REQUIRED BY THE STUDENTS. THE MAJOR PURPOSE OF INPLANT TRAINING WOULD BE TO POLISH THE KNOWLEDGE ACQUIRED THROUGH ACADEMICS WITH THE ENHANCED ASSISTANCE OF INDUSTRY PRACTICES. STUDENTS WHO HAVE UNDERGONE INPLANT TRAINING ARE WELCOMED BY THE COMPANIES AS THEY ARE WELL ACQUAINTED WITH SUCH KIND OF TRAININGS. IT IS LIKELY THAT THE ACADEMIC STUDIES ONLY IMPARTS THEORETICAL KNOWLEDGE TO THE STUDENTS AND THESE INPLANT TRAININGS PAVE WAY FOR STUDENTS TO GAIN A PRACTICAL KNOWLEDGE OF WHAT THEY LEARNT THEORETICALLY AND FROM COMPUTERS. WITH INPLANT TRAININGS, STUDENTS CAN EXPERIENCE THE EXPOSURE TOWARDS INDUSTRIAL HAPPENINGS, AS THEY ARE LIABLE TO WORK IN SUCH CONDITIONS RIGHT AFTER THEIR STUDIES.

BENEFITS OF IN-PLANT TRAINING

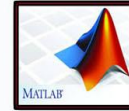
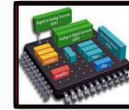
IN PLANT TRAINING (IPT) WILL OFFER AN INDUSTRIAL EXPOSURE TO THE COLLEGE STUDENTS IN ORDER TO DEVELOP THEIR CAREER IN THE MANNER FOR OF HIGH TECH INDUSTRIAL NEEDS. WE ARE OFFERING IN PLANT TRAINING TO THE STUDENT. HERE STUDENTS ARE BASICALLY GETTING GUIDED SO AS TO COME OUT OF THEIR INTEREST IN DIFFERENT STREAMS AND WHAT ARE THE FUNDAMENTAL CONCEPTS THEY UNDERSTAND ON THAT SPECIFIC DOMAIN. AFTER THE SUCCESSFUL COMPLETION OF STUDIES, EVERY STUDENT HAS TO FACE THIS COMPETITIVE WORLD WITH THEIR GAINED KNOWLEDGE AND SKILL TO FACE MANY DIFFICULTIES AND ACQUIRE SUITABLE SOLUTIONS THAT HAS TO SOLVE IN MINIMUM PERIOD OF TIME. THIS SORT OF TRAINING ALLOWS YOU TO GET MORE PRACTICAL KNOWLEDGE.

JOB ORIENTED PROGRAMS

- * REDHAT LINUX**
- * CCNA**
- * AWS**
- * BLOCKCHAIN**

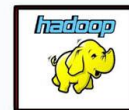
ELECTRONICS AND COMMUNICATION/ELECTRONICS & INSTRUMENTATION

- EMBEDDED SYSTEM
- INTERNET OF THINGS(IOT)
- MATLAB(SIGNAL AND IMAGE PROCESSING)
- PYTHON
- NETWORK SIMULATOR
- LABVIEW
- CIRCUIT DESIGN
- SIMULINK
- ETHICAL HACKING



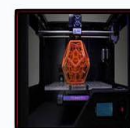
COMPUTER SCIENCE AND INFORMATION SCIENCE ENGINEERING

- WEB DEVELOPMENT
- MOBILE APP DEVELOPMENT(ANDROID)
- PHP DEVELOPMENT
- JAVA
- PYTHON DEVELOPMENT
- MACHINE LEARNING
- DATA SCIENCE
- TESTING
- MATLAB(IMAGE AND SIGNAL PROCESSING)
- INTERNET OF THINGS
- ETHICAL HACKING
- CLOUD COMPUTING
- DATA MINING
- BIG DATA/HADOOP



MECHANICAL ENGINEERING COURSES

- CATIA
- ANSYS
- HYPERMESH
- AUTOCAD
- GD&T
- 3D PRINTING
- NASTRAN
- SOLIDWORKS
- CNC SIMULATION
- FUSION 360



CIVIL ENGINEERING COURSES

- ETABS
- SAP2000
- AUTOCAD
- REVIT
- PRIMAVERA
- 3DS MAX
- SAFE
- ANSYS
- GEOSLOPE
- GEOHECRAS



PERSONALITY DEVELOPMENT

- PRESENTATION & COMMUNICATION SKILLS
- GROOMING SKILLS
- PERSONALITY DEVELOPMENT
- CUSTOMER SERVICE
- INTERVIEW SKILLS

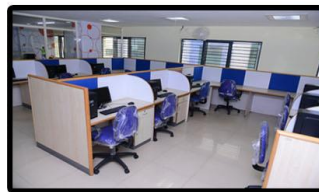
PLACEMENT PROGRAM

- 100% PLACEMENT ASSISTANCE
- INTERVIEW Q & A PROVIDED WHICH ARE FREQUENTLY ASKED IN COMPANIES
- OUR EXPERIENCED TRAINERS WILL HELP GET YOUR RESUME READY AS PER IT STANDARDS
- REAL CASE STUDIES/EXAMPLES PROVIDED TO CLEAR INTERVIEWS EASILY
- PRACTICAL KNOWLEDGE TRAINING ON EVERY TOPIC

FULLY FLEDGED ENVIRONMENT WITH HI-TECH SMART CLASSROOM



COMPUTER LAB FACILITY FOR COURSES ALSO FOR R & D



OUR RESPECTED CLIENTS AND TIE UP COMPANIES AND COLLEGES



ADDRESS:

HARIHAR: 4TH MAIN , 4TH CROSS, J.C EXTENSION , 577601

**DAVANGERE: ANJANEYA EXTENSION, OPPOSITE ANJANEYASWAMY TEMPLE,
NEAR UBBDT BOYS HOSTEL ROAD KARNATAKA - 577004**

PROJECT LIST

SL NO	TITLE
SIRINVL01	A Design Technique for Faster Dadda Multiplier
SIRINVL02	An Efficient Retouched Bloom Filter Based Word-Matching Stage Of BLASTN:
SIRINVL03	VLSI Implementation of Single Cycle Access Structure for Logic Test in FPGA Technology:
SIRINVL04	Design of accumulator Based 3-Weight Pattern Generation using LP-LSFR
SIRINVL05	Sleepy Stack Approach Based Low Power Flip Flop
SIRINVL06	Low Power High Performance Double Tail Comparator
SIRINVL07	Design of a Low Drop-Out Voltage Regulator using VLSI
SIRINVL08	VLSI Architecture of Arithmetic Coder Used in SPIHT
SIRINVL09	FPGA Based ECG Signal Noise Suppression
SIRINVL10	Low Power and High Speed Conditional Push-Pull Pulsed Latches
SIRINVL11	An Enhanced Low Cost High Performance Image Scaling Processor Using VLSI
SIRINVL12	Design of Finite Impulse Response Filter Using Distributed Arithmetic of Lookup Table
SIRINVL13	Pipelined Radix-2k Feedforward FFT Architectures
SIRINVL14	Performance Analysis of High Efficiency Low Density Parity-Check Code Decoder for Low Power Applications
SIRINVL15	Advanced Encryption System to Improve the System Computing Speed
SIRINVL16	AMBA-Advanced High Performance Bus IP Block
SIRINVL17	A Multichannel Multimode RF Transceiver with DSM
SIRINVL18	Asynchronous Transfer Mode Knockout Switch Concentrator
SIRINVL19	Behavioral Synthesis of Asynchronous Circuits
SIRINVL20	Building an AMBA AHB Compliant Memory Controller
SIRINVL21	Implementation of Carry Tree Adder
SIRINVL22	Fixed Angle of Rotation Using CORDIC Designs
SIRINVL23	Design of FPGA based 32-bit Floating Point Arithmetic Unit
SIRINVL24	Design and Synthesis of a Field Programmable CRC Circuit Architecture
SIRINVL25	Design of an On-Chip Permutation Network for Multiprocessor SOC

SIRINVL26	VLSI Architecture for Visible Watermarking in a Secure Still Digital Camera (S2DC) Design
SIRINVL27	VHDL Environment for Floating Point Arithmetic Logic Unit
SIRINVL28	Design and Implementation of High Speed DDR SDRAM Controller
SIRINVL29	Design and Synthesis of QPSK
SIRINVL30	Design of Multi Value Logic Using Quantum Dot Gate FET
SIRINVL31	Design and Simulation of FFT Processor Using Radix-4 Algorithm Using FPGA
SIRINVL32	Fuzzy based PID Controller using VHDL for Transportation Application

